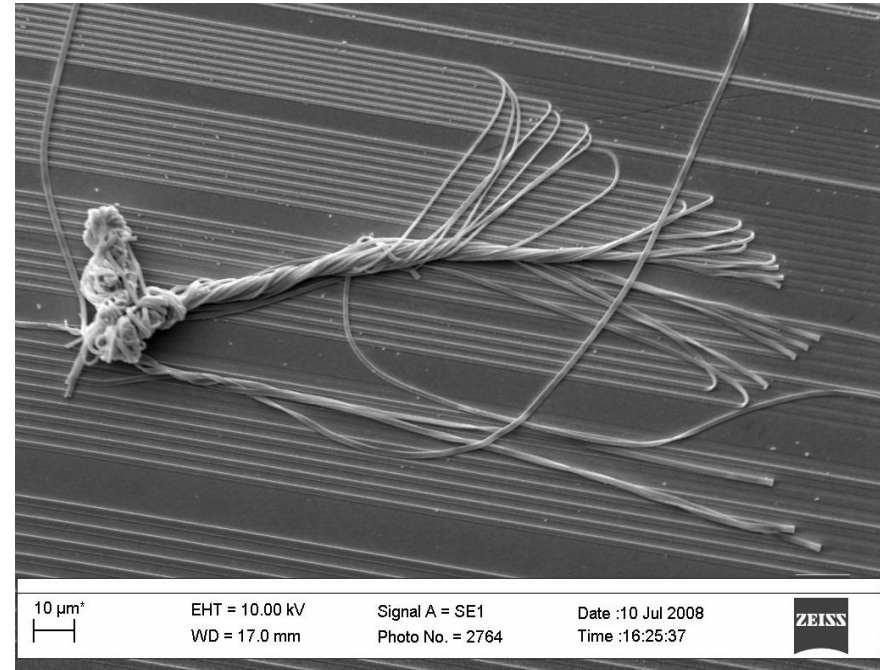


Binary Semiconductor Nanowires Through Templated Electrodeposition

Nanoscale well-defined heterojunction semiconductor structures are desired for efficient photovoltaic devices. CHM Researchers have created **nanowires** of cadmium selenide (CdSe) on indium tin oxide (ITO) substrate using a simple electrochemical deposition process and polymer templates created by nanoimprint lithography. The wires are ~400 nm wide and span length of the substrate (1 cm x 1 cm).



*Professors D. Venkataraman and Ken Carter,
University of Massachusetts*